6ES7313-5BG04-0AB0

## **Data sheet**



SIMATIC S7-300, CPU 313C, Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 3 high-speed counters (30 kHz), Integr. power supply 24 V DC, work memory 128 KB, Front connector (2x 40-pole) and Micro Memory Card required

| General information   |   |
|---|---|
| Product type designation                                      | CPU 313C  |
| HW functional status  | 01  |
| Firmware version  | V3.3  |
| Engineering with  |   |
| Programming package   | STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203                 |
| Supply voltage  |   |
| Rated value (DC)  | 24 V  |
| permissible range, lower limit (DC)                           | 19.2 V  |
| permissible range, upper limit (DC)                           | 28.8 V  |
| external protection for power supply lines (recommendation)   | Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A |
| Mains buffering   |   |
| Mains/voltage failure stored energy time                      | 5 ms  |
| Repeat rate, min.   | 1 s   |
| Load voltage L+   |   |
| Digital inputs  |   |
| — load voltage / at digital input / at DC / rated value       | 24 V  |
| <ul> <li>Reverse polarity protection</li> </ul>               | Yes   |
| Digital outputs   |   |
| — Rated value (DC)  | 24 V  |
| <ul> <li>Reverse polarity protection</li> </ul>               | No  |
| Input current   |   |
| Current consumption (rated value)                             | 650 mA  |
| Current consumption (in no-load operation), typ.              | 150 mA  |
| Inrush current, typ.  | 5 A   |
| l²t   | 0.7 A <sup>2</sup> ·s   |
| Digital inputs  |   |
| <ul> <li>from load voltage L+ (without load), max.</li> </ul> | 80 mA   |
| Digital outputs   |   |
| • from load voltage L+, max.                                  | 50 mA   |
| Power loss  |   |
| Power loss, typ.  | 12 W  |
| Memory  |   |
| Work memory   |   |
| • integrated  | 128 kbyte   |
| expandable  | No  |
| Load memory   |   |
| • Plug-in (MMC)   | Yes   |
| <ul><li>Plug-in (MMC), max.</li></ul>                         | 8 Mbyte   |

| • Data management on MMC (after last programming), | 10 a   |
|--|--|
| min.   |  |
| Backup   | Voca Citaranteed by MMC (restitutions from   |
| • present  | Yes; Guaranteed by MMC (maintenance-free)  |
| without battery                                    | Yes; Program and data  |
| CPU processing times                               | 0.07   |
| for bit operations, typ.                           | 0.07 μs  |
| for word operations, typ.                          | 0.15 µs  |
| for fixed point arithmetic, typ.                   | 0.2 µs   |
| for floating point arithmetic, typ.                | 0.72 µs  |
| CPU-blocks   |  |
| Number of blocks (total)                           | 1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.                  |
| DB   | 1000000 57 410 111110 00001  |
| Number, max.                                       | 1 024; Number range: 1 to 16000  |
| • Size, max.                                       | 64 kbyte   |
| FB   | , and the second se |
| Number, max.                                       | 1 024; Number range: 0 to 7999   |
| • Size, max.                                       | 64 kbyte   |
| FC   | , and the second se |
| Number, max.                                       | 1 024; Number range: 0 to 7999   |
| • Size, max.                                       | 64 kbyte   |
| OB   |  |
| Number, max.                                       | see instruction list   |
| • Size, max.                                       | 64 kbyte   |
| Number of free cycle OBs                           | 1; OB 1  |
| Number of time alarm OBs                           | 1; OB 10   |
| <ul> <li>Number of delay alarm OBs</li> </ul>      | 2; OB 20, 21   |
| Number of cyclic interrupt OBs                     | 4; OB 32, 33, 34, 35   |
| Number of process alarm OBs                        | 1; OB 40   |
| Number of startup OBs                              | 1; OB 100  |
| Number of asynchronous error OBs                   | 4; OB 80, 82, 85, 87   |
| Number of synchronous error OBs                    | 2; OB 121, 122   |
| Nesting depth                                      |  |
| per priority class                                 | 16   |
| additional within an error OB                      | 4  |
| Counters, timers and their retentivity             |  |
| S7 counter   |  |
| Number   | 256  |
| Retentivity  |  |
| — adjustable                                       | Yes  |
| — preset   | Z 0 to Z 7   |
| Counting range                                     |  |
| — lower limit                                      | 0  |
| — upper limit                                      | 999  |
| IEC counter  |  |
| • present  | Yes  |
| <ul> <li>Type</li> </ul>                           | SFB  |
| • Number   | Unlimited (limited only by RAM capacity)   |
| S7 times   |  |
| • Number   | 256  |
| Retentivity  |  |
| — adjustable                                       | Yes  |
| — preset   | No retentivity   |
| Time range   |  |
| — lower limit                                      | 10 ms  |
| — upper limit                                      | 9 990 s  |
| IEC timer  |  |
| • present  | Yes  |
| • Type   | SFB  |
| Number   | Unlimited (limited only by RAM capacity)   |
|  |  |

| Data areas and their retentivity                          |   |
|---|---|
| Retentive data area (incl. timers, counters, flags), max. | 64 kbyte                                |
| Flag  |   |
| • Size, max.  | 256 byte                                |
| Retentivity available                                     | Yes; MB 0 to MB 255                     |
| Retentivity preset  | MB 0 to MB 15                           |
| Number of clock memories                                  | 8; 1 memory byte                        |
| Data blocks   |   |
| Retentivity adjustable                                    | Yes; via non-retain property on DB      |
| Retentivity preset  | Yes                                     |
| Local data  |   |
| per priority class, max.                                  | 32 kbyte; Max. 2048 bytes per block     |
| Address area  |   |
| I/O address area  |   |
| • Inputs  | 1 024 byte                              |
| Outputs   | 1 024 byte                              |
| of which distributed                                      | ·                                       |
| — Inputs  | none                                    |
| — Outputs   | none                                    |
| Process image   |   |
| • Inputs  | 1 024 byte                              |
| Outputs   | 1 024 byte                              |
| <ul> <li>Inputs, adjustable</li> </ul>                    | 1 024 byte                              |
| Outputs, adjustable                                       | 1 024 byte                              |
| • Inputs, default   | 128 byte                                |
| Outputs, default  | 128 byte                                |
| Default addresses of the integrated channels              | ·                                       |
| — Digital inputs  | 124.0 to 126.7                          |
| Digital outputs   | 124.0 to 125.7                          |
| — Analog inputs   | 752 to 761                              |
| — Analog outputs  | 752 to 755                              |
| Digital channels  |   |
| • Inputs  | 1 016                                   |
| — of which central  | 1 016                                   |
| <ul><li>Outputs</li></ul>                                 | 1 008                                   |
| of which central  | 1 008                                   |
| Analog channels   |   |
| • Inputs  | 253                                     |
| — of which central  | 253                                     |
| <ul><li>Outputs</li></ul>                                 | 250                                     |
| — of which central  | 250                                     |
| Hardware configuration                                    |   |
| Number of expansion units, max.                           | 3                                       |
| Number of DP masters                                      |   |
| integrated  | none                                    |
| • via CP  | 4                                       |
| Number of operable FMs and CPs (recommended)              |   |
| • FM  | 8                                       |
| • CP, PtP   | 8                                       |
| • CP, LAN   | 6                                       |
| Rack  |   |
| Racks, max.   | 4                                       |
| Modules per rack, max.                                    | 8; In rack 3 max. 7                     |
| Time of day   |   |
| Clock   |   |
| Hardware clock (real-time)                                | Yes                                     |
| retentive and synchronizable                              | Yes                                     |
| Backup time   | 6 wk; At 40 °C ambient temperature      |
| Deviation per day, max.                                   | 10 s; Typ.: 2 s                         |
| Behavior of the clock following POWER-ON                  | Clock continues running after POWER OFF |
|   | <u> </u>                                |

| - Debaying of the cleak fellowing evains of healths navied  | the clear continues at the time of day it had when never was switched off   |
|---|---|
| Behavior of the clock following expiry of backup period   | the clock continues at the time of day it had when power was switched off   |
| Operating hours counter   | ,   |
| • Number  | 1   |
| Number/Number range   | 0   |
| Range of values   | 0 to 2^31 hours (when using SFC 101)  |
| <ul> <li>Granularity</li> </ul>   | 1 h   |
| • retentive   | Yes; Must be restarted at each restart  |
| Clock synchronization   |   |
| <ul><li>supported</li></ul>   | Yes   |
| ● to MPI, master  | Yes   |
| • on MPI, device  | Yes   |
| • in AS, master   | Yes   |
| • in AS, device   | No  |
| Digital inputs  |   |
| Number of digital inputs  | 24  |
| of which inputs usable for technological functions  | 12  |
| integrated channels (DI)  | 24  |
| Input characteristic curve in accordance with IEC 61131, type 1   | Yes   |
| Number of simultaneously controllable inputs  |   |
| horizontal installation   |   |
| — up to 40 °C, max.   | 24  |
| — up to 60 °C, max.   | 12  |
| vertical installation   |   |
| — up to 40 °C, max.   | 12  |
| Input voltage   |   |
| Rated value (DC)  | 24 V  |
| • for signal "0"  | -3 to +5V   |
| • for signal "1"  | +15 to +30 V  |
| Input current   |   |
| • for signal "1", typ.  | 8 mA  |
| Input delay (for rated value of input voltage)  |   |
| for standard inputs   |   |
| — parameterizable   | Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.) |
| — Rated value   | 3 ms  |
| for technological functions   | 31113   |
| — at "0" to "1", max.   | 16 μs; Minimum pulse width/minimum pause between pulses at maximum  |
| — at 0 to 1, max.   | counting frequency  |
| Cable length  |   |
| shielded, max.  | 1 000 m; 100 m for technological functions  |
| unshielded, max.  | 600 m; for technological functions: No  |
| for technological functions   |   |
| — shielded, max.  | 100 m; at maximum count frequency   |
| — unshielded, max.  | not allowed   |
| Digital outputs   |   |
|   | 16  |
| Number of digital outputs   |   |
| of which high-speed outputs  integrated channels (DO)   | 4; Notice: You cannot connect the fast outputs of your CPU in parallel  |
| integrated channels (DO)  | 16  |
| Short-circuit protection  | Yes; Clocked electronically   |
| Response threshold, typ.  I in the first of industries about the control of the first of industries about the control of the first | 1 A   |
| Limitation of inductive shutdown voltage to   | L+ (-48 V)  |
| Controlling a digital input   | Yes   |
| Switching capacity of the outputs   |   |
| • on lamp load, max.  | 5 W   |
| Load resistance range   |   |
| • lower limit   | 48 Ω  |
| • upper limit   | 4 kΩ  |
| Output voltage  |   |
| • for signal "1", min.  | L+ (-0.8 V)   |
| Output current  |   |
|   |   |

| <ul><li>for signal "1" rated value</li></ul>                          | 500 mA   |
|---|--|
| <ul><li>for signal "1" permissible range, min.</li></ul>              | 5 mA   |
| <ul><li>for signal "1" permissible range, max.</li></ul>              | 0.6 A  |
| <ul><li>for signal "1" minimum load current</li></ul>                 | 5 mA   |
| <ul> <li>for signal "0" residual current, max.</li> </ul>             | 0.5 mA   |
| Parallel switching of two outputs                                     |  |
| <ul><li>for uprating</li></ul>  | No   |
| <ul> <li>for redundant control of a load</li> </ul>                   | Yes  |
| Switching frequency   |  |
| with resistive load, max.   | 100 Hz   |
| <ul> <li>with inductive load, max.</li> </ul>                         | 0.5 Hz   |
| • on lamp load, max.  | 100 Hz   |
| <ul> <li>of the pulse outputs, with resistive load, max.</li> </ul>   | 2.5 kHz  |
| Total current of the outputs (per group)                              |  |
| horizontal installation   |  |
| — up to 40 °C, max.   | 3 A  |
| — up to 60 °C, max.   | 2 A  |
| vertical installation   | 27   |
|   | 2 A  |
| — up to 40 °C, max.   | 40   |
| Cable length  | 1,000 m  |
| • shielded, max.  | 1 000 m  |
| • unshielded, max.  | 600 m  |
| Analog inputs   |  |
| Number of analog inputs   | 4  |
| <ul> <li>For voltage/current measurement</li> </ul>                   | 4  |
| For resistance/resistance thermometer measurement                     | 1  |
| integrated channels (AI)  | 5; 4x current/voltage, 1x resistance   |
| permissible input voltage for current input (destruction limit), max. | 5 V; Permanent   |
| permissible input voltage for voltage input (destruction limit), max. | 30 V; Permanent  |
| permissible input current for voltage input (destruction limit), max. | 0.5 mA; Permanent  |
| permissible input current for current input (destruction limit), max. | 50 mA; Permanent   |
| Electrical input frequency, max.                                      | 400 Hz   |
| No-load voltage for resistance-type transmitter, typ.                 | 3.3 V  |
| Constant measurement current for resistance-type transmitter, typ.    | 1.25 mA  |
| Technical unit for temperature measurement adjustable                 | Yes; Degrees Celsius / degrees Fahrenheit / Kelvin                                       |
| Input ranges  |  |
| <ul> <li>Voltage</li> </ul>   | Yes; ±10 V / 100 k $\Omega$ ; 0 V to 10 V / 100 k $\Omega$                               |
| • Current   | Yes; ±20 mA / 100 $\Omega$ ; 0 mA to 20 mA / 100 $\Omega$ ; 4 mA to 20 mA / 100 $\Omega$ |
| Resistance thermometer  | Yes; Pt 100 / 10 M $\Omega$  |
| Resistance  | Yes; 0 $\Omega$ to 600 $\Omega$ / 10 M $\Omega$  |
| Input ranges (rated values), voltages                                 |  |
| • 0 to +10 V  | Yes  |
| — Input resistance (0 to 10 V)  | 100 kΩ   |
| Input ranges (rated values), currents                                 |  |
| • 0 to 20 mA  | Yes  |
| — Input resistance (0 to 20 mA)                                       | 100 Ω  |
| • -20 mA to +20 mA  | Yes  |
| — Input resistance (-20 mA to +20 mA)                                 | 100 Ω  |
|   |  |
| • 4 mA to 20 mA   | Yes  |
| — Input resistance (4 mA to 20 mA)                                    | 100 Ω  |
| Input ranges (rated values), resistance thermometer                   | V  |
| • Pt 100  | Yes  |
| — Input resistance (Pt 100)   | 10 ΜΩ  |
| Input ranges (rated values), resistors                                |  |
| • 0 to 600 ohms   | Yes  |
| — Input resistance (0 to 600 ohms)                                    | 10 ΜΩ  |
| Thermocouple (TC)   |  |

| Temperature compensation  |   |
|---|---|
| — parameterizable   | No  |
| Characteristic linearization  |   |
| parameterizable   | Yes; by software  |
| — for resistance thermometer  | Pt 100  |
| Cable length  | 11100   |
| shielded, max.  | 100 m   |
| Analog outputs  | 100 111   |
|   |   |
| integrated channels (AO)  | 2   |
| Voltage output, short-circuit protection  | Yes   |
| Voltage output, short-circuit current, max.   | 55 mA   |
| Current output, no-load voltage, max.   | 14 V  |
| Output ranges, voltage  |   |
| • 0 to 10 V   | Yes   |
| • -10 V to +10 V  | Yes   |
| Output ranges, current  |   |
| • 0 to 20 mA  | Yes   |
| • -20 mA to +20 mA  | Yes   |
| • 4 mA to 20 mA   | Yes   |
| Connection of actuators   |   |
| <ul> <li>for voltage output two-wire connection</li> </ul>  | Yes; Without compensation of the line resistances   |
| <ul> <li>for voltage output four-wire connection</li> </ul>   | No  |
| <ul> <li>for current output two-wire connection</li> </ul>  | Yes   |
| Load impedance (in rated range of output)   |   |
| <ul><li>with voltage outputs, min.</li></ul>  | 1 kΩ  |
| <ul> <li>with voltage outputs, capacitive load, max.</li> </ul>   | 0.1 μF  |
| <ul><li>with current outputs, max.</li></ul>  | 300 Ω   |
| <ul> <li>with current outputs, inductive load, max.</li> </ul>  | 0.1 mH  |
| Destruction limits against externally applied voltages and currents   |   |
| Voltages at the outputs towards MANA  | 16 V; Permanent   |
| Current, max.   | 50 mA; Permanent  |
| Cable length  |   |
| shielded, max.  | 200 m   |
| Analog value generation for the inputs  |   |
| Measurement principle   | Actual value encryption (successive approximation)  |
| Integration and conversion time/resolution per channel  | rotas value one, passi (eaccooli e approximatori)   |
|   |   |
|   | 12 hit  |
| • Resolution with overrange (bit including sign), max.  | 12 bit Yes: 16.6 / 20 ms  |
| <ul><li>Resolution with overrange (bit including sign), max.</li><li>Integration time, parameterizable</li></ul>  | Yes; 16.6 / 20 ms   |
| • Resolution with overrange (bit including sign), max.  |   |
| <ul> <li>Resolution with overrange (bit including sign), max.</li> <li>Integration time, parameterizable</li> <li>Interference voltage suppression for interference</li> </ul>  | Yes; 16.6 / 20 ms   |
| <ul> <li>Resolution with overrange (bit including sign), max.</li> <li>Integration time, parameterizable</li> <li>Interference voltage suppression for interference frequency f1 in Hz</li> </ul>   | Yes; 16.6 / 20 ms<br>50 / 60 Hz   |
| <ul> <li>Resolution with overrange (bit including sign), max.</li> <li>Integration time, parameterizable</li> <li>Interference voltage suppression for interference frequency f1 in Hz</li> <li>Time constant of the input filter</li> </ul>  | Yes; 16.6 / 20 ms<br>50 / 60 Hz<br>0.38 ms  |
| <ul> <li>Resolution with overrange (bit including sign), max.</li> <li>Integration time, parameterizable</li> <li>Interference voltage suppression for interference frequency f1 in Hz</li> <li>Time constant of the input filter</li> <li>Basic execution time of the module (all channels</li> </ul>  | Yes; 16.6 / 20 ms<br>50 / 60 Hz<br>0.38 ms  |
| <ul> <li>Resolution with overrange (bit including sign), max.</li> <li>Integration time, parameterizable</li> <li>Interference voltage suppression for interference frequency f1 in Hz</li> <li>Time constant of the input filter</li> <li>Basic execution time of the module (all channels released)</li> </ul>  | Yes; 16.6 / 20 ms<br>50 / 60 Hz<br>0.38 ms  |
| <ul> <li>Resolution with overrange (bit including sign), max.</li> <li>Integration time, parameterizable</li> <li>Interference voltage suppression for interference frequency f1 in Hz</li> <li>Time constant of the input filter</li> <li>Basic execution time of the module (all channels released)</li> </ul> Analog value generation for the outputs  | Yes; 16.6 / 20 ms<br>50 / 60 Hz<br>0.38 ms  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms   |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms   |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Encoder   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Encoder  Connection of signal encoders  | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms   |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms   |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  Fincoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Froder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes  |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for inductive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection   | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances    |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection  for resistance measurement with three-wire connection | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection  for resistance measurement with four-wire connection  | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances    |
| Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection  for resistance measurement with three-wire connection | Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No |

| permissible quiescent current (2-wire sensor), max.   | 1.5 mA   |
|---|--|
| Errors/accuracies   |  |
| Temperature error (relative to input range), (+/-)  | 0.006 %/K  |
| Crosstalk between the inputs, min.  | 60 dB  |
| Repeat accuracy in steady state at 25 °C (relative to input range), (+/-)   | 0.06 %   |
| Output ripple (relative to output range, bandwidth 0 to 50 kHz), (+/-)  | 0.1 %  |
| Linearity error (relative to output range), (+/-)   | 0.15 %   |
| Temperature error (relative to output range), (+/-)   | 0.01 %/K   |
| Crosstalk between the outputs, min.   | 60 dB  |
| Repeat accuracy in steady state at 25 °C (relative to output range), (+/-)  | 0.06 %   |
| Operational error limit in overall temperature range  |  |
| Voltage, relative to input range, (+/-)   | 1 %  |
| Current, relative to input range, (+/-)   | 1 %  |
| Resistance, relative to input range, (+/-)  | 1 %  |
| Voltage, relative to output range, (+/-)  | 1 %  |
| Current, relative to output range, (+/-)  | 1 %  |
| Basic error limit (operational limit at 25 °C)  |  |
| Voltage, relative to input range, (+/-)   | 0.8 %; Linearity error ±0.06 %   |
| Current, relative to input range, (+/-)   | 0.8 %; Linearity error ±0.06 %   |
| Resistance, relative to input range, (+/-)  | 0.8 %; Linearity error ±0.2 %  |
| Resistance thermometer, relative to input range, (+/-)  | 0.8 %  |
|   | 0.8 %  |
| Voltage, relative to output range, (+/-)     Current relative to output range, (+/-)  |  |
| Current, relative to output range, (+/-)  | 0.8 %  |
| Interference voltage suppression for f = n x (f1 +/- 1 %), f1 = interference  | 30 dB  |
| <ul> <li>Series mode interference (peak value of interference &lt; rated value of input range), min.</li> </ul>   | 40 dB  |
| Common mode interference, min.  | 40 db  |
| Interfaces  |  |
| Number of PROFINET interfaces   | 0  |
| Number of RS 485 interfaces   | 1; MPI   |
| Number of RS 422 interfaces   | 0  |
| 1. Interface  |  |
| Interface type  | Integrated RS 485 interface  |
| Isolated  | No   |
| Interface types   |  |
| • RS 485  | Yes  |
| Output current of the interface, max.   | 200 mA   |
| Protocols   |  |
| • MPI   | Yes  |
| <ul> <li>PROFIBUS DP master</li> </ul>  | No   |
| PROFIBUS DP device  | No   |
| Point-to-point connection   | No   |
| MPI   |  |
| Transmission rate, max.   | 187.5 kbit/s   |
| Services  |  |
|   |  |
| — PG/OP communication   | Yes  |
| <ul><li>— PG/OP communication</li><li>— Routing</li></ul>   | Yes<br>No  |
|   |  |
| — Routing   | No   |
| Routing     Global data communication   | No<br>Yes<br>Yes   |
| <ul><li>Routing</li><li>Global data communication</li><li>S7 basic communication</li><li>S7 communication</li></ul>   | No Yes Yes Yes; Only server, configured on one side  |
| <ul> <li>Routing</li> <li>Global data communication</li> <li>S7 basic communication</li> <li>S7 communication</li> <li>S7 communication, as client</li> </ul>   | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB         |
| <ul> <li>Routing</li> <li>Global data communication</li> <li>S7 basic communication</li> <li>S7 communication</li> <li>S7 communication</li> <li>S7 communication, as client</li> <li>S7 communication, as server</li> </ul>                            | No Yes Yes Yes; Only server, configured on one side  |
| Routing     Global data communication     S7 basic communication     S7 communication     S7 communication     S7 communication, as client     S7 communication, as server  | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB Yes     |
| Routing     Global data communication     S7 basic communication     S7 communication     S7 communication     S7 communication, as client     S7 communication, as server  Protocols  PROFIsafe  | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB         |
| Routing     Global data communication     S7 basic communication     S7 communication     S7 communication     S7 communication, as client     S7 communication, as server  Protocols  PROFIsafe  communication functions / header                      | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB Yes  No |
| Routing     Global data communication     S7 basic communication     S7 communication     S7 communication, as client     S7 communication, as server  Protocols  PROFIsafe  communication functions / header  PG/OP communication                      | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB Yes  No |
| Routing     Global data communication     S7 basic communication     S7 communication     S7 communication, as client     S7 communication, as server  Protocols  PROFIsafe  communication functions / header  PG/OP communication  Data record routing | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB Yes  No |
| Routing     Global data communication     S7 basic communication     S7 communication     S7 communication, as client     S7 communication, as server  Protocols  PROFIsafe  communication functions / header  PG/OP communication                      | No Yes Yes Yes; Only server, configured on one side No; but via CP and loadable FB Yes  No |

| <ul> <li>Number of GD loops, max.</li> </ul>  | 8   |
|---|---|
| <ul> <li>Number of GD packets, max.</li> </ul>  | 8   |
| <ul> <li>Number of GD packets, transmitter, max.</li> </ul>   | 8   |
| <ul> <li>Number of GD packets, receiver, max.</li> </ul>  | 8   |
| <ul> <li>Size of GD packets, max.</li> </ul>  | 22 byte   |
| <ul> <li>Size of GD packet (of which consistent), max.</li> </ul>   | 22 byte   |
| S7 basic communication  |   |
| supported   | Yes   |
| User data per job, max.   | 76 byte   |
| User data per job (of which consistent), max.   | 76 byte; 76 bytes (with X SEND or X RCV); 64 bytes (with X PUT or X GET   |
|   | as server)  |
| S7 communication  |   |
| • supported   | Yes   |
| as server   | Yes   |
| ● as client   | Yes; Via CP and loadable FB   |
| User data per job, max.   | 180 byte; With PUT/GET  |
| User data per job (of which consistent), max.   | 240 byte; as server   |
| S5 compatible communication   | 240 byte, as server   |
| ·   | Vac: via CP and leadable FC   |
| • supported   | Yes; via CP and loadable FC   |
| Number of connections   |   |
| • overall   | 8   |
| <ul> <li>usable for PG communication</li> </ul>   | 7   |
| <ul> <li>reserved for PG communication</li> </ul>   | 1   |
| <ul> <li>adjustable for PG communication, min.</li> </ul>   | 1   |
| <ul> <li>adjustable for PG communication, max.</li> </ul>   | 7   |
| <ul> <li>usable for OP communication</li> </ul>   | 7   |
| <ul> <li>reserved for OP communication</li> </ul>   | 1   |
| <ul> <li>adjustable for OP communication, min.</li> </ul>   | 1   |
| adjustable for OP communication, max.   | 7   |
| usable for S7 basic communication   | 4   |
| reserved for S7 basic communication   | 0   |
| adjustable for S7 basic communication, min.   | 0   |
| •   |   |
| — adjustable for S7 basic communication, max.   | 4   |
| S7 message functions  | 0 D   |
| Number of login stations for message functions, max.  | 8; Depending on the configured connections for PG/OP and S7 basic communication   |
| Process diagnostic messages   |   |
| •   | Yes   |
| simultaneously active Alarm-S blocks, max.  | Yes 300   |
| •   |   |
| simultaneously active Alarm-S blocks, max.  |   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  | 300   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  | Yes; Up to 2 simultaneously   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step   | Yes; Up to 2 simultaneously Yes   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  | Yes; Up to 2 simultaneously Yes   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block Single step Number of breakpoints Status/control   | Yes; Up to 2 simultaneously Yes 4 Yes   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters  |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block Single step Number of breakpoints Status/control  Status/control  Variables Number of variables, max.  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block Single step Number of breakpoints Status/control  • Status/control variable • Variables • Number of variables, max. — of which status variables, max.  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30  |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing   | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes  |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  • Forcing, variables   | Yes; Up to 2 simultaneously Yes  4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  Forcing  Forcing  Number of variables, max.   | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes  |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  • Forcing, variables  • Number of variables, max.  Diagnostic buffer  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  • Forcing, variables  • Number of variables, max.   | Yes; Up to 2 simultaneously Yes  4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  • Forcing, variables  • Number of variables, max.  Diagnostic buffer  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  • Forcing, variables  • Number of variables, max.  Diagnostic buffer  • present   | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10  Yes  |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing  • Forcing  • Forcing, variables  • Number of variables, max.  Diagnostic buffer  • present  • Number of entries, max.  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10  Yes 500  |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  adjustable  | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10  Yes 500 No   |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  adjustable  of which powerfail-proof   | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10  Yes 500 No 100; Only the last 100 entries are retained                         |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block  Single step  Number of breakpoints  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  adjustable  of which powerfail-proof  Number of entries readable in RUN, max.  adjustable | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10  Yes Soo No 100; Only the last 100 entries are retained 499                     |
| simultaneously active Alarm-S blocks, max.  Test commissioning functions  Status block Single step Number of breakpoints Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  adjustable  of which powerfail-proof  Number of entries readable in RUN, max.                | Yes; Up to 2 simultaneously Yes 4  Yes Inputs, outputs, memory bits, DB, times, counters 30 30 14  Yes Inputs, outputs 10  Yes Soo No 100; Only the last 100 entries are retained 499 Yes; From 10 to 499 |

| • can be read out  | Yes  |
|--|--|
| Interrupts/diagnostics/status information                  |  |
|  |  |
| Diagnostics indication LED                                 | V  |
| Status indicator digital input (green)                     | Yes  |
| Status indicator digital output (green)                    | Yes  |
| Integrated Functions                                       |  |
| Counter  |  |
| <ul> <li>Number of counters</li> </ul>                     | 3; See "Technological Functions" manual                                  |
| Counting frequency, max.                                   | 30 kHz   |
| Frequency measurement                                      | Yes  |
| Number of frequency meters                                 | 3; up to 30 kHz (see "Technological Functions" manual)                   |
| controlled positioning                                     | No   |
| integrated function blocks (closed-loop control)           | Yes; PID controller (see "Technological Functions" manual)               |
| PID controller   | Yes  |
| Number of pulse outputs                                    | 3; Pulse width modulation up to 2.5 kHz (see "Technological Functions"   |
| Limit for any control                                      | Manual)  |
| Limit frequency (pulse)                                    | 2.5 kHz  |
| Potential separation                                       |  |
| Potential separation digital inputs                        |  |
| Potential separation digital inputs                        | Yes  |
| between the channels                                       | No   |
| between the channels and backplane bus                     | Yes  |
| Potential separation digital outputs                       |  |
| <ul> <li>Potential separation digital outputs</li> </ul>   | Yes  |
| <ul> <li>between the channels</li> </ul>                   | Yes  |
| <ul> <li>between the channels, in groups of</li> </ul>     | 8  |
| between the channels and backplane bus                     | Yes  |
| Potential separation analog inputs                         |  |
| <ul> <li>Potential separation analog inputs</li> </ul>     | Yes; common for analog I/O   |
| <ul> <li>between the channels</li> </ul>                   | No   |
| <ul> <li>between the channels and backplane bus</li> </ul> | Yes  |
| Potential separation analog outputs                        |  |
| <ul> <li>Potential separation analog outputs</li> </ul>    | Yes; common for analog I/O   |
| <ul> <li>between the channels</li> </ul>                   | No   |
| <ul> <li>between the channels and backplane bus</li> </ul> | Yes  |
| Isolation  |  |
| Isolation tested with                                      | 600 V DC   |
| Ambient conditions   |  |
| Ambient temperature during operation                       |  |
| • min.   | 0 °C   |
| • max.   | 60 °C  |
| configuration / header                                     |  |
| Configuration software                                     |  |
| STEP 7   | Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP |
| OILI 1   | 203  |
| STEP 7 Lite  | No   |
| configuration / programming / header                       |  |
| Command set  | see instruction list   |
| Nesting levels   | 8  |
| System functions (SFC)                                     | see instruction list   |
| System function blocks (SFB)                               | see instruction list   |
| Programming language                                       |  |
| — LAD  | Yes  |
| — FBD  | Yes  |
| — STL  | Yes  |
| — SCL  | Yes  |
| — SCL<br>— CFC   | Yes  |
|  |  |
| — GRAPH  | Yes  |
| — HiGraph®   | Yes  |
| Know-how protection  | Vec  |
| User program protection/password protection                | Yes  |

| <ul> <li>Block encryption</li> </ul> | Yes; With S7 block Privacy |
|--------------------------------------|----------------------------|
| Dimensions                           |                            |
| Width                                | 120 mm                     |
| Height                               | 125 mm                     |
| Depth                                | 130 mm                     |
| Weights                              |                            |
| Weight, approx.                      | 660 g                      |

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